Sandip Foundation's
Sandip Institute of Technology & Research Centre
Department of Electronics & Telecommunication Engineering
ME E&TC Result Analysis
AY 2013-14 (2013 Course)

Sr. No.	Name of Subject	Total No. of Students Appeared	No. of Student Passed	Percentage of Result
		Sem-I (Dec-2013)		
1	Digital CMOS Design	17	18	88.23%
2	Embedded System Design	17	15	88.23%
3	Reconfigurable Computing	17	16	94.11
4	Research Methodology	17	17	100.00%
5	Wireless Sensor Network (E-I)	17	17	100.00%
Name of Topper		**Baitade Anita T.**		**8.16 SGPA**
		Sem-II (May-2014)		
1	Analog CMOS Design	17	15	88.23%
2	System-on-Chip	17	17	100.00%
3	Embedded Signal Processors	17	17	100.00%
4	Embedded Product Design (E-II)	17	17	100.00%
Name of Topper		**Bhadane Dinesh S.**		**8.36**

Prof P A Dhulekar
ME Co ordinator

Prof Dr Gayatri M Phade
HOD E&TC Dept.