



**Sandip Foundation's  
Sandip Institute of Technology and Reserach Centre, Nashik  
Department of Computer Engineering**

# **Activity Report**

**Of**

**One Day Expert Lecture**

**On**

**“Digital Electronics and Logic Design”  
(07<sup>th</sup> July 2017)**

**Organized by,  
Sandip Foundation's  
Sandip Institute of Technology and Research Center**

**Department of Computer Engineering**

**Name of Event :**

One day expert lecture on “Digital Electronics and Logic Design”.

**Date :**

07<sup>th</sup> July 2017

**Duration :**

One Day (3 Hrs)

**Resource Person :**

Prof. S. V. Kendre

**Address of Resource Person:**

Govt. Resi. Women's Poly. Latur

**Event Coordinators:**

Prof. K. B. Mahajan (Asst. Professor, SITRC, Nashik)

**Participants :**

SE (Comp.) 159

**Venue:**

Seminar hall (Computer Department)

**Aim:**

To acquire the basic knowledge of Digital Logic level and application and to understand Digital Electronics circuits.

**Objective:**

- Use in applications where constant decreasing in size and employing more complex technology involved.
- To make it simple and interesting to learn.

**Outcomes :**

Participants learned

- Have a thorough understanding of the fundamental concepts and techniques used in digital electronics.

To understand and examine the structure of various number systems and its application in digital design.

- The ability to understand, analyze and design various combinational and sequential circuits.
- Ability to identify basic requirements for a design application and propose a cost effective solution.

### **Summary Report of “Digital Electronics and Logic Design”**

Department of Computer Engg. Organized Expert Lecture on 'Digital Eletronics & Logic Design' delivered by Prof. Sandip Kendre, Lecturer, Govt. Resi. Women's Poly., Latur for SE Computer students. He has more than 7 Years of experience in engineering teaching field. The programme began with the introduction of the eminent guest by Prof.K.B.Mahajan The guest then felicitated by Prof. A. D. Potgantwar, HOD, Computer. Felicitation of guest followed by a very insightful lecture from our guest speaker, Prof. Sandip Kendre. He explained various topics related to TTL and CMOS.

### **Photographs :**



**Prof. K. B. Mahajan**  
**Co-ordinator**

**Prof.(Dr.) A. D. Potgantwar**  
**(HOD, Comp Dept.**