



**SANDIP FOUNDATION'S
SANDIP INSTITUTE OF TECHNOLOGY & RESEARCH CENTRE, Nashik
DEPARTMENT OF ELECTRICAL ENGINEERING
SE-1st SEM
Academic Year 2016-17**

Report on Guest Lecture

Event Title: Guest Lecture on “Analog and Digital Electronics”

Event Date: 10th Sept 2016

Event Conduction Duration: 1.30 pm to 3.00 pm

Event Venue: SE Class Room, University Building

Target Students: SE Electrical Engg.

Event Resource Person Details: Prof.Dr.Sanjiv Sharma, Jhulelal
Institute of Technology , Nagpur

Name of Event Co-ordinator: 1. Prof.Dr.P.G.Burade
2. Prof.A.V.Satpute

Electrical Department, SITRC, Nashik.

Objectives:

- To demonstrate the concept of numbering system & Boolean's algebra reduction using K map.
- To design and analyze sequential and combinational circuits.
- To develop the concept of basics of operational Amplifier and its applications.
- To introduction to BJT and diode rectifier.

Outcome:

- Students had a thorough idea about concepts and terminology of

analog and digital electronics.

- Students comprehended the use of logic to design and creation, uses of gates, sequential circuits.
- Students got conversant with the transistor family and OPAMPs and their applications.



Prof.Dr.Sanjiv Sharma Interacting with students and explaining the Fundamentals of ADE.





Prof.Dr.Sanjiv Sharma explaining K-Map and Booleans Algebra to Students

