



Sandip Foundation's
Sandip Institute of Technology & Research Centre, Nashik
Department of Electronics & Telecommunication

Date: 3rd October 2015

Name of Event: “Electronic Circuit Exhibition”

Event coordinator : Prof. V. P.Jagtap

Objectives:

- 1.To integrate skills in electronic design among students, through their curriculum.Students must learn how to apply their existing abilities to new areas, as well as adopt many new skills.
2. Participating in Exhibition helps to develop a the confidence and competence among students, and fosters a spirit of technical inquiry
- 3.Exhibition is an experience that can be challenging and self-validating for the students in Electronic design area.

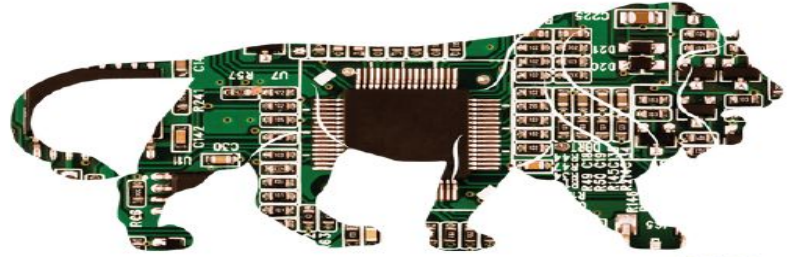
Outcome:

1. Electronic circuit Exhibition stimulates the to hidden ideas in students mind which leads to entrepreneurship
2. Electronic circuit Exhibition put seeds in students mind to think application oriented
3. Electronic circuit Exhibition is a little contribution to Make in India.

Photos:



Electronic Circuit Exhibition -2015 (Innovation & Design)



In association with IEI & under the subject ESED....

Date- 3rd Oct 2015
Venue-Measurement Lab
Time- 3 to 5



Organized By-
E&TC Department
SITRC, Nashik



