



Sandip Foundation's

Sandip Institute of Technology & Research Centre, Nashik

Department of Electronics & Telecommunication

STTP-2014 Report

Name of Event:- One week short Term Training Program on “**Advanced Trends in VLSI & Embedded Signal Processing**” From 24th Feb 2014 to 28th Feb 2014

Organizer: Department of Electronics & Telecommunication Engineering

Objective: The Objective of workshop is to bring together the faculties, research scholars and PG Students from around the country to discuss various aspects of VLSI and Embedded signal processing System. The workshop will provide opportunities to enhance practical skill and knowledge to those who are specialized in VLSI and Embedded System Design

About STTP: VLSI & Embedded System are playing a key role in modern Electronics Engineering. Keeping this in view, a one week workshop on “VLSI & Embedded System” is being organized to focus on latest concepts and applications in VLSI and Embedded Signal processing. Within this course the following points has been covered.

- Circuit Designing with Analog CMOS, LNA Design
- Modeling with SOC, Hardware and software interfaces, Micro-programmed architecture and RTL Intent
- Design & applications of adaptive filters, DSP Processor architecture, Memory system, Data transfer, code optimization
- DTMF Generation and detection, FFT and wavelet algorithms
- Low Power VLSI Design
- Hands on Each session (using Tools like Cadence, Microwind, TCAD etc.)

Chief Guest of Event: This event has been inaugurated by Chief Guest Dr. Omprakash Kulkarni from Mentor, Adviser & Consulting Engineer, Dr. S. T. Gandhe, Principal SITRC, Dr. N. S. Murthy, NIT Warangal, & Prof. Mrs. G. M. Phade, HOD E&TC.

Resource Persons:

Name of the Resource Person	Institute
Dr. N. S. Murthy	NIT Warangal
Mr. Ankush Shingade	Electronics Department, University of Pune
Dr. S. S. Mande	Don Bosco College of Engineering, Mumbai
Dr. Sanjay Pawar	Principal, UMIT , SNDT Mumbai
Prof. Amitkumar Khade	Cummins College of Engineering Pune
Prof. Shastry PVS	Cummins College of Engineering Pune

Outcomes of STTP:

The participants for this STTP are PG students and faculties of PG course 'VLSI & Embedded signal processing' comes from the various Engineering institute of the state. They are very glad by the content delivery from the various resource persons on the topic advanced Trends in VLSI & Embedded signal processing. They got the all basic concept related to the various fields in the VLSI, & Embedded signal processing, manufacturing technology that, how it will realized the technology in our real life.

Also they know the various jobs, Industries & research opportunity in the said field for their personal as well as social development through Ph.D and PG.

For the improvement in the Quality of education as well as Teaching such Programs will be organized frequently, this type of complements are welcomed from the participants in the valedictory function of this event

Event Photographs:



