



Sandip Foundation's
Sandip Institute of Technology & Research Centre, Nashik
Department of Electronics & Telecommunication Engineering

Name of Event: “PCB Design using Advanced Simulation Tool”
[In Association with IEI Student's Chapter, E & TC]

Date: 28th - 30th January, 2015.

Event Co-ordinator:

Prof. O. S. Vaidya
Asst. Prof. (E & TC Dept.)
SITRC, Nashik.

Name of Resource Persons with Address:

- 1) Prof. S. S. Chopade – Asso. Prof. (E & TC Dept., SITRC, Nashik)
- 2) Prof. P. G. Salunke - Asst. Prof. (E & TC Dept., SITRC, Nashik)

Objectives:

- 1) To introduce various Latest PCB Design Simulation Tools.
- 2) Get familiar with types of PCB, Schematics versus Layout Format.
- 3) To design footprint, editing and routing in PCB using TINA software.
- 4) To design and implement Power supply design.

Outcomes:

- 1) Students understood the basic design steps of PCB.
- 2) Participants are explored with different library files of TINA software and designed schematics.
- 3) Students designed Schematics versus Layout format for simple power supply design.
- 4) Students came to know rules for tracking side and successfully implemented power supply design.

Photos:

