Faculty Name: Dr.Bhagwat kakde Designation: Associate Professor Mail ID: bhagwat.kakde@sitrc.org Contact No: +91- 9993521297,8770061443	

BIO

I, Dr.Bhagwat Kakde is an Associate Professor in the Electronics and Telecommunication Engineering department of SITRC, Nashik having 16 years of teaching Experience and area of expertise in Digital Communication Engineering ,He received his Ph.D. in Electronics and Communication Engineering and his Master of Engineering in Digital Communication Engineering and BE in Electronics and Communication Engineering.His areas of interest Digital Communication Engineering, Digital image processing, wireless communication, VLSI and OFDM communication. Two Phd Awarded under my guidance and two Phd Thesis Submitted and I had supervised 25 Mtech/ME Project Thesis. All Papers are published in various reputed journals with standard indexing like Springer, Scopus , UGC,National and International Jouranal.He is associated with professional bodies like, **IAEEEE Member Number: 71050351**, **IAENG Member No: 212026** ,IAOE, The IRED membership ID is AM101000583431. His total experience is 16 years comprises of academic, administration, research development, mentoring teachers and students etc

Qualification:

    1. B.E. (Electronics & Communication Engineering)

    2. M.E. (Digital Communication Engineering)

    3. Ph.D. (Electronics and Communication Engineering)

Research Details:

    1. International Journals: 50

2. International Conferences: 12

3. National Conferences: 15

4.**Scopus indexed journals: 05**

5.**springer indexed journals:1**

 6. **SCI:01**

7. **Book: Intelligent Sustainable Systems**

https://link.springer.com/chapter/10.1007/978-981-19-7663-6_17

8. **Ph D. Supervised : 2 Nos**

Phd Submitted :   2Nos

 Research IDs: 0000-0002-7205-1048

Area Research Work: 1.Digital Communication

2.OFDM

3.Digital Signal Processing

Experience:

1. Teaching experience : 16 Years

Subjects:

1. Digital Communication
2. Digital Electronics
3. Basic Electronics
4. Electronics Devices and circuit
5. VlSI Design